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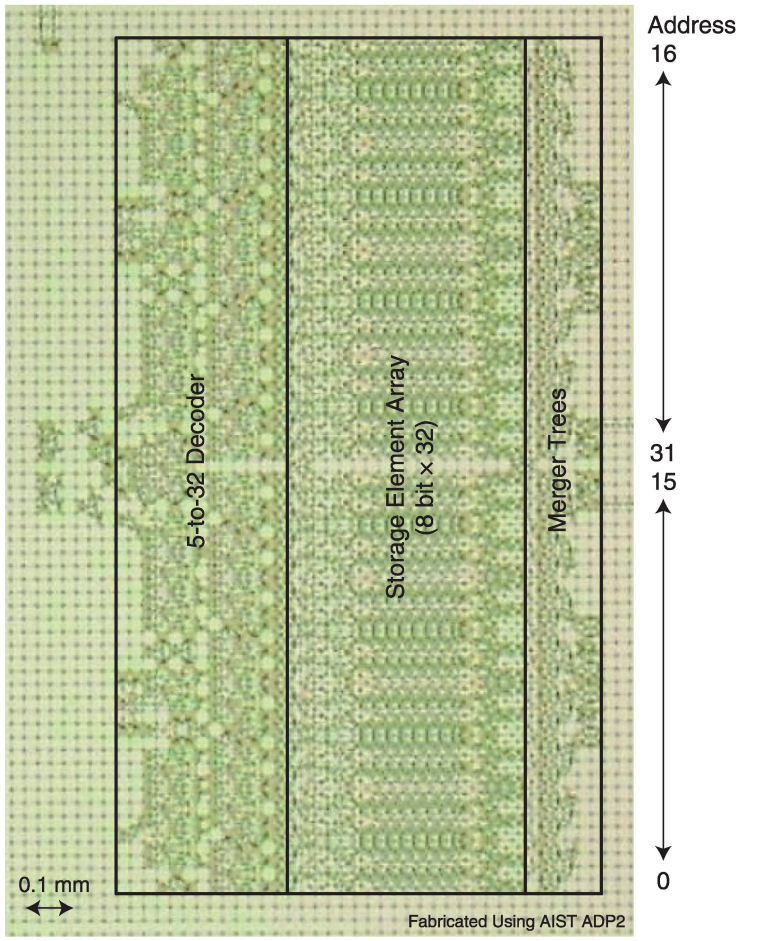

Figure 5 from ''High-Density Shift-Register-Based Rapid Single-Flux-Quantum Memory System for Bit-Serial Microprocessors,'' by Tanaka *et al.*, 1301005.

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