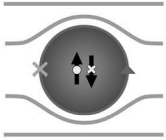


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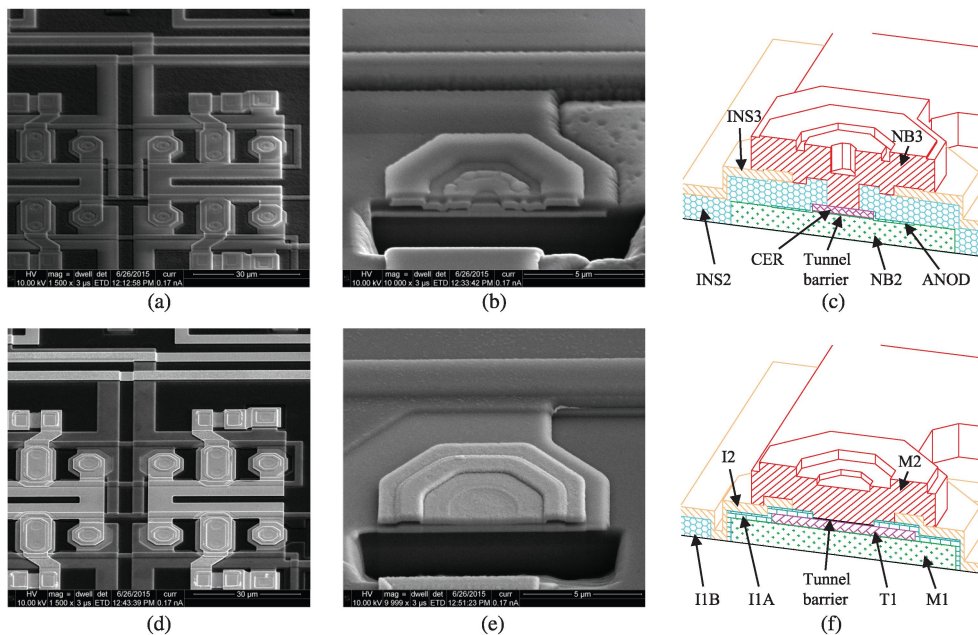
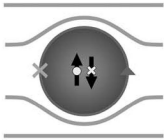


Figure 1 from "Multilayer Fabrication Process for Josephson Junction Circuits Cross-Compatible Over Two Foundries," by Kiviranta *et al.*, 1100905.

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