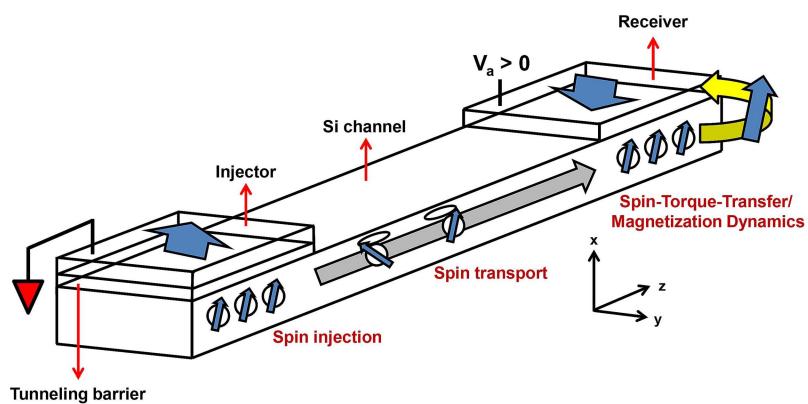


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Schematics of an Si interconnect for all-spin logic. From the paper,
“Design and Analysis of Si Interconnects for All-Spin Logic,”
by S.-C. Chang, S. Manipatruni, D. E. Nikonov,
I. A. Young, and A. Naeemi, on p. 3400513.

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