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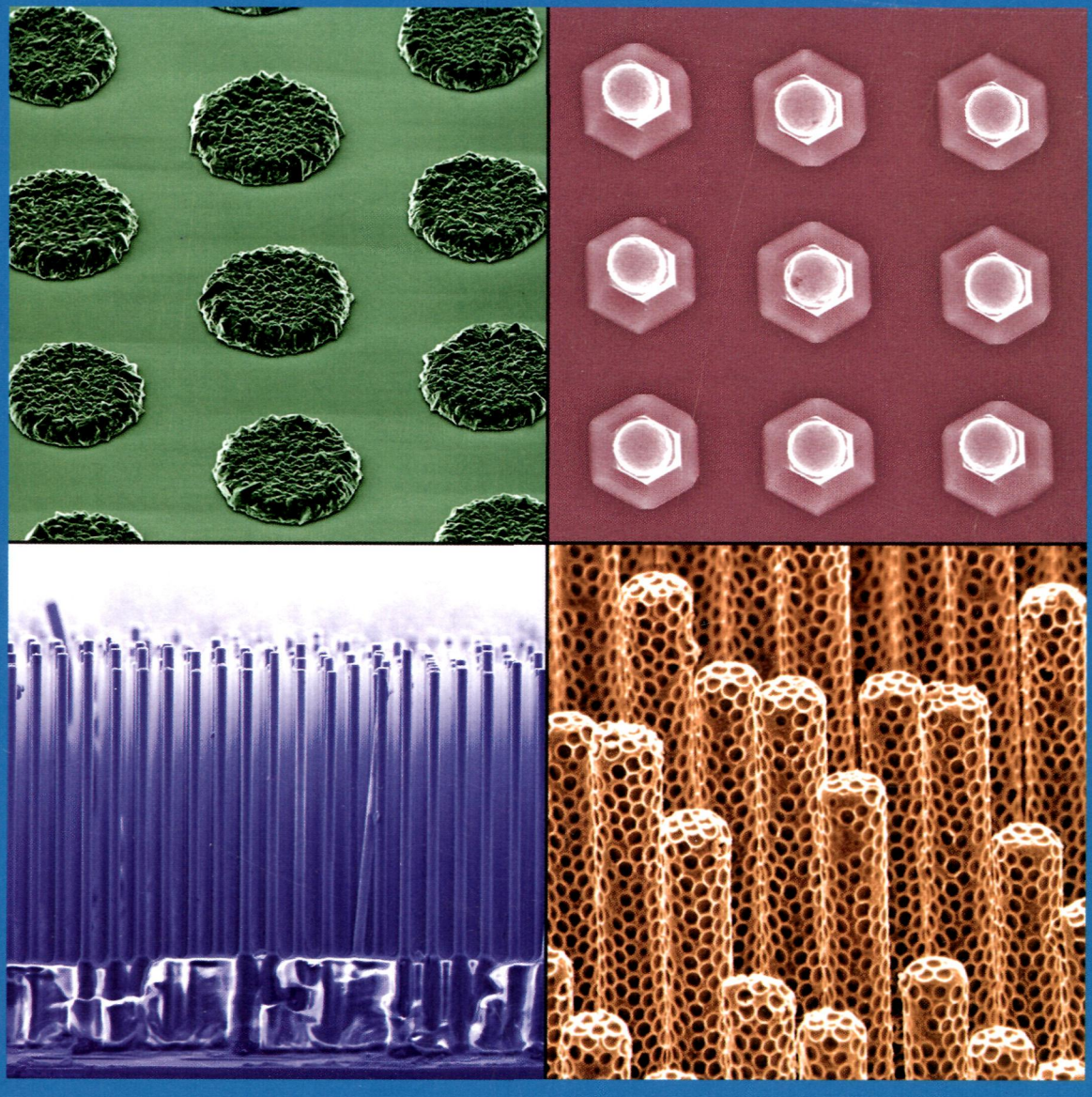

Si Microwire Arrays Grown by the Vapor–Liquid–Solid Process in Different Stages of Device Fabrication (see page 5A)

ENERGY CONVERSION AND STORAGE, OPTICAL AND ELECTRONIC DEVICES, INTERFACES, NANOMATERIALS, AND HARD MATTER

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ON THE COVER: Si microwire arrays grown by the vapor−liquid−solid process in different stages of device fabrication. Upper left: Si wafer patterned with islands of electrodeposited Cu for VLS growth. Upper right: Top-down view of VLS-grown Si microwires. Bottom left: Cross-section of Si microwire array embedded in PDMS and removed from growth substrate. Bottom right (image courtesy of Robert Coridan): Si microwires coated with a hierarchically structured templated film of WO_3. See page 747.

Feature Article

Articles

Energy Conversion and Storage; Energy and Charge Transport

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